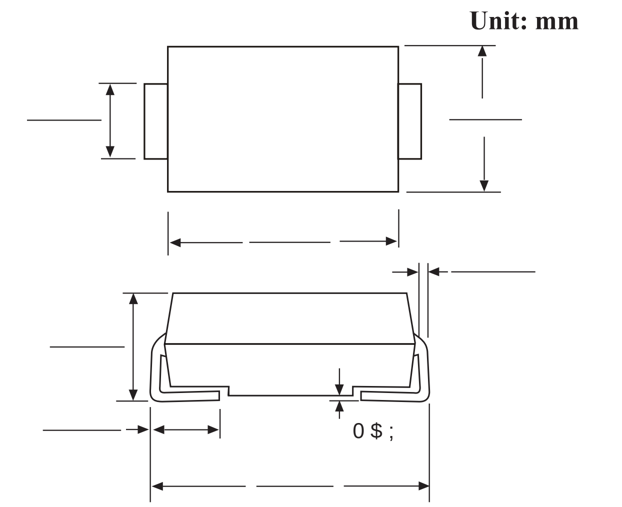
SM % PLASTIC SILICON RECTIFIERS

FEATURES

- ”Plastic package has Underwriters Laboratory Flammability Classification 94V-O Utilizing
- ”Metal silicon junction ,majoritycarrier conduction
- ”Built-in strain relief
- ”For surfacemounted applications
- ”Low power loss ,high efficiency,High surge capability
- ”High current capability ,Low forward voltage drop
- ”For use in low voltage ,high frequency inverters, free wheeling ,and polarity protection applications
- ”High temperature soldering guaranteed:260 ℃/10 seconds at terminals
- ”Component in accordanceto RoHS and WEEE (8



MECHANICAL DATA

- ”Case: SM % molded plastic body
- ”Terminals:Lead solderable per MIL-STD-750,method 2026
- ”Polarity:Color band denotes cathode end

MAXIMUM RATINGS AND CHARACTERISTICS

ƒ &$PELHQW7HPSHUDWXUHXQOHVVRWKHUZLVHQRWHG

TYPE NUMBER		SYMBOL	SS 2	S 6	SS 4	SS 5	SS 6	SS 8	SS 1	UNITS
Maximum recurrent peak reverse voltage		V_{RRM}	20	30	40	50	60	80	1 0	V
Maximum RMS voltage		V_{RMS}	14	21	28				7	V
Maximum DC blocking voltage		V_{DC}	20	30	40	50	60	80	1	V
Maximum Average Forward rectified Current0.375"(9.5mm) lead length		$I_{F(AV)}$	.0							A
Peak forw ard surge current 8.3ms single half sine-w ave superimposed on rated load		I_{FSM}	0.0							A
Maximum instantaneous forward voltage at .0 A(Note1)		V_F	0.			0.		0.		V
Maximum reverse current at rated DC blocking voltage perdiode	@T_A=25	I_R								mA
	@T_A=100		0.0			10.0				
7\SLFDO 7KHUPDO 5HVLVWDQFH 1RWH		$R_{\theta JA}$ 5 -/								/W
q·®§¡~ª=³¬¡²§-¬=¡~®~¡§²~¬¡£Ek-²£⊧F		`g	00							S)
Storage Temperature		T_{STG}	- ---- + 150							
Operation Junction Temperature		T_j	- 5 ---- + 125			- ---- + 150				

Notes: N. m³ª±£⊧£±²⁄⊧PMM⊧X⊧®³ª±£⊧µ§¢²¦INB⊧¢³²·⊧¡·¡ª£

O. ⊧ª`K_K⊧« -³¬²£¢⊧MKRR v ⊧Z - ®³@⊧®~¢⊧2 @£ 2 ± O., £ 2 ²³ A £¢⊧2 ¢⊧N, [ὤ ⊧2 ¬¢⊧A £'A ³ @±£⊧« -ª²~±£⊧ ¢⊧]